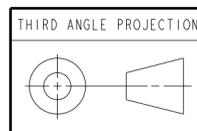


1. MODULE IS DESIGNED TO BE SUFFICIENTLY COOLED BY A PXI_e CHASSIS THAT MEETS PICMG (COMPACT PCI EXPRESS) STANDARDS. CARE MUST BE TAKEN TO PROVIDE ADEQUATE AIRFLOW AROUND THE CHASSIS AND TO REGULARLY INSPECT AND CLEAN CHASSIS FAN FILTERS TO MAINTAIN OPTIMAL AIRFLOW OVER THE MODULE.

NOTES: (UNLESS OTHERWISE SPECIFIED)



<p>UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS SHOWN IN INCHES [MILLIMETERS]</p> <p>TOLERANCES ARE: .XX [0.X] ±.01 [0.25] .XXX [0.XX] ±.005 [0.13] ANGLES: ±2° MATERIAL</p> <p>FINISH</p>	<p>REVISION NOTES</p> <p>1. INITIAL RELEASE.</p>	<p>DATE</p>
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<p>SignalCore Inc. AUSTIN, TEXAS</p>			
<p>TITLE SC5407A, 6 GHz UPCONVERTER, PXI_e CUSTOMER DRAWING</p>			
<p>SIZE C</p>	<p>CAGE CODE NO. 6CG47</p>	<p>DATE OF DRAWING 04-26-16</p>	<p>REV 1</p>
<p>SCALE: 1/1</p>	<p>DO NOT SCALE DRAWING</p>	<p>SHEET 1 OF 1</p>	