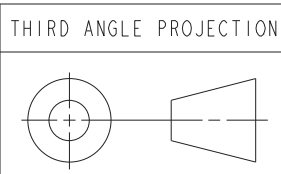


1. MODULE IS DESIGNED TO BE SUFFICIENTLY COOLED BY A PXIe CHASSIS THAT MEETS PICMG (COMPACT PCI EXPRESS) STANDARDS. CARE MUST BE TAKEN TO PROVIDE ADEQUATE AIRFLOW AROUND THE CHASSIS AND TO REGULARLY INSPECT AND CLEAN CHASSIS FAN FILTERS TO MAINTAIN OPTIMAL AIRFLOW OVER THE MODULE.

NOTES: (UNLESS OTHERWISE SPECIFIED)



<u>UNLESS OTHERWISE SPECIFIED</u> ALL DIMENSIONS SHOWN IN INCHES [MILLIMETERS]		<u>REVISION NOTES</u>		<u>DATE</u>	
TOLERANCES ARE: .XX [0.X]±.01 [0.25] .XXX [0.XX]±.005 [0.13] ANGLES:±2° MATERIAL		1. INITIAL RELEASE.		08-24-15	
FINISH		SignalCore Inc. AUSTIN, TEXAS			
		TITLE SC5405A, 3.9 GHz UPCONVERTER, PXIe CUSTOMER DRAWING			
SIZE C		CAGE CODE NO. 6CG47		DATE OF DRAWING 08-24-15	
SCALE: 1/1		DO NOT SCALE DRAWING		REV 1	
		SHEET 1		OF 1	